

Registration No. 28,421

Docket No. 219723US2S

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hajime NAGANO, et al.

SERIAL NO: NEW APPLICATION

FILING DATE: HEREWITH

FOR: SEMICONDUCTOR DEVICE USING PARTIAL SOI SUBSTRATE AND MANUFACTURING METHOD THEREOF

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	25 - 20 =	5	× \$18 =	\$90.00
INDEPENDENT CLAIMS	3 - 3 =	0	× \$84 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$280 =	\$0.00
<input type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$0.00
BASIC FEE				\$740.00
TOTAL OF ABOVE CALCULATIONS				\$830.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
<input checked="" type="checkbox"/> RECORDATION OF ASSIGNMENT			+ \$40 =	\$40.00
TOTAL				\$870.00

- ☐ Please charge Deposit Account No. 15-0030 in the amount of \$0.00 A duplicate copy of this sheet is enclosed.
- ☒ A check in the amount of \$870.00 to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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